

Appl. No. 10/039,113
Amdt. Dated 10/14/2004
Reply to Office Action of July 28, 2004

Amendments to the Specification:

Please replace the paragraph that begins on page 6, line 1, with the following paragraph:

Figure 3 illustrates a flow diagram of an exemplary retirement routine 300 in accordance with an embodiment of the invention. The steps operations of the retirement routine are taken after a successful execution of an instruction. In stepblock 302, the retirement logic unit 222 sets the executed flag in ROB entry corresponding to the instruction. In stepblock 304, the retirement logic unit 222 determines whether there is a destination real register 217 for the instruction. If there is no destination real register 217 for the instruction, the retirement routine 300 ends. If, on the other hand, there is a destination real register 217 for the instruction, in stepblock 306 the retirement logic unit 222 determines whether the real register 217 is designated to undergo the retirement routine 300 in accordance with the invention (i.e., whether the register is one listed in the data commitment table 216).

Please replace the paragraph that begins on page 6, line 22, with the following paragraph:

Accordingly, if in stepblock 306 the retirement logic unit 222 determines that the real register be written to is exempt from the new retirement routine 300, then in stepblock 308 the retirement logic unit 222 causes the copying of the resulting data from the alias register to the real register. Otherwise, in stepblock 308, the retirement logic unit 222 causes the setting of the valid data bit in the ROB entry pertaining to that instruction. In stepblock 310, the retirement logic unit 222 reads the committed value location field of the data commitment table 216 corresponding to the real register to determine if the previous register value is in the real register or in an alias register. If the retirement logic unit 222 determines that the previous register value is in an alias register, in stepblock 314 the retirement logic unit 222 causes a deasserting of the valid data bit of the ROB entry pointed to by the data commitment table 216. Then in stepblock 316 the retirement logic unit 222 causes the writing of the ROB entry index of the instant instruction to the ROB entry index field of the data commitment table 216 corresponding to the real register associated with the new data, and modifies the committed data location field to indicate that the register value is in an alias register pointed to by the corresponding ROB entry index field. If, on the other hand, in stepblock 314 the retirement logic unit 222 determines that

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the previous register value is in the RRF ~~216~~²¹⁷, the retirement logic unit 222 just performs the function specified in stepblock 316 as previously discussed.

Please replace the paragraph that begins on page 7, line 4, with the following paragraph:

The new retirement routine 300 saves an alias register –to-real register copying stepblock (relative to the prior art retirement routine) each time the retirement routine 300 performs stepblock 314. This situation occurs when the same real register is written to (actually written to its alias in the ROB) by two or more instructions within the same instruction window (the size of the ROB). This is substantially different than the prior art retirement routine that makes an alias register-to-real register copy each time an instruction retires. Whereas the new retirement routine 300, avoids some of these copies, and in theory, can eliminate essentially 100 percent of the register writes if the code reuses results extensively, e.g. a long series of “inc eax; inc eax; inc eax, ...”. Accordingly, the reduction in real register copying has the beneficial results of lower power consumption, extended battery life and a less sophisticated cooling system for the processor, among other benefits.

Please replace the paragraph that begins on page 7, line 23, with the following paragraph:

Specifically, the stepblock in 402 the allocator 214 locates the next ROB entry n for a new instruction. In stepblock 404, the allocator 214 reads the valid data field of the next ROB entry n to determine whether the corresponding alias register contains valid data. If not, the allocator 214 proceeds to stepblock 412 to add the new instruction into the next ROB entry n. If, however, the valid data field indicates that the next ROB entry n has valid data, in stepblock 406 the allocator 214 causes the content in the alias register of the next ROB entry n to be copied in to the corresponding real register 217. In stepblock 408, the allocator 214 deasserts the valid data bit in the next ROB entry n since the new instruction has not been executed, and therefore the next ROB entry n has yet to have valid data. Then in stepblock 410 the allocator 214 modifies the “committed value location” field of the data commitment table 216 to indicate that the register value for the corresponding real register is now in the real register 217. Finally, in stepblock 412 the allocator 214 causes the new instruction to be added into the next ROB entry n.

Please replace the paragraph that begins on page 8, line 1, with the following paragraph:

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In the case that there has been a branch misprediction, or other control flow altering event, like an exception, all the non-committed registers younger than the branch in the ROB 215 are invalid. In the prior art processor system, all non-committed register are discarded by setting the renamer tables to point to all the registers last value to RRF. However, according to the new processor system 200, some of the committed data will reside in the ROB 215. According to the processor 200 of the invention, this can be dealt with in two manners. The first option is to copy the committed data in the ROB 215 to the RRF ~~216-217~~ in the time the pipeline fills up again. The second option is to make the pointers in the renamer to point to the ROB entry that the data commitment table indicates. For example, if an instruction that writes to the EAX register is committed from the ROB entry index 31, the data commitment table entry corresponding to the EAX will contain the number 31 in the corresponding ROB entry index field. After a branch misprediction, the renamer will now point to the last value of the EAX to ROB entry 31. An instruction that has a source the register EAX, will get its source renamed to ROB entry 31, so it will get the correct data.

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